



## **DKM Upgrading Switch from 3.04 to 3.05**

**Version: A**

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**This document outlines the steps taken to update the DKM Comact or Modular switch to version 3.05 from 3.04.**

**You can use the files found in the folder “Design\_FPGA-Update”.**

**Step 1:** If your DKM system runs Matrix firmware older than **V3.04 from June 22<sup>nd</sup>, 2017**, you will have to update to the mentioned firmware first. This firmware is part of the firmware package and is named “**Step1\_20170622\_V0304.tfw**”.

**Step 2:** Update the FPGA design by launching the new 3.5.5.3 DKM Java Utility, and going to **Administration>>Miscellaneous>>FPGA Update**, and selecting the **DSGNCPU.ffw** file for the FPGA design for the Controller Board. Make sure the “**Update Primary Design**” is unchecked for the first update.

The screenshot shows the 'Miscellaneous' tab in the DKM Java Utility. The 'FPGA Update' sub-tab is active. The 'Firmware File (\*.ffw)' field is set to 'DSGNCPU.ffw'. The 'Update Progress' bar is at 0%. A log window displays the following information:

2018-02-19T10:57:15.247	Selected firmware:
2018-02-19T10:57:15.263	DSGNCPU F03.05 2018-02-17

The 'Update primary design' checkbox is unchecked and highlighted with a red box.

**Step 3:** After the update a pop-up window will appear asking to restart the switch, select **NO** and go to the next step.

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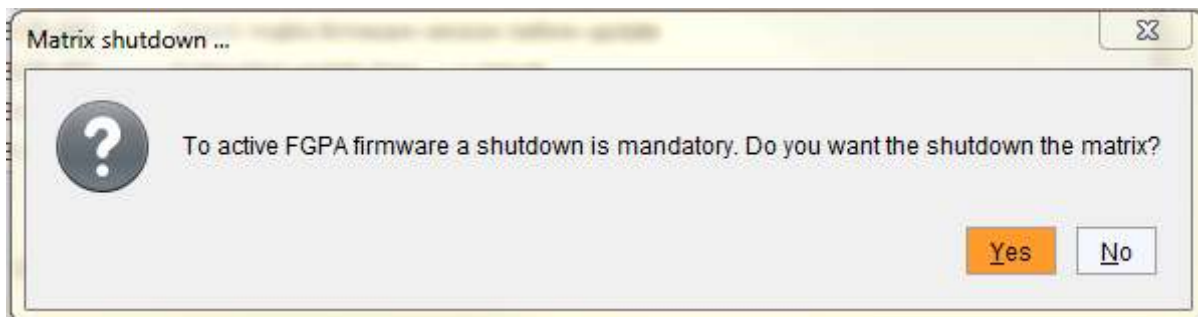
**Step 4:** Repeat Step 2, but check the box “**Update Primary Design**”. This will be the second update, but with the Update Primary Design box checked.

The screenshot shows a software interface with an orange header labeled "Miscellaneous". Below the header are three tabs: "License Management", "FPGA Update", and "OSD Update". The "OSD Update" tab is active. The interface includes a "Firmware File (\*.ffw)" field containing "DSGNCPU.ffw" and a "Browse..." button. Below this is an "Update Progress" section with a progress bar at 0% and an "Update" button. A log area shows two entries: "2018-02-19T11:02:39.488 Selected firmware:" and "2018-02-19T11:02:39.488 DSGNCPU F03.05 2018-02-17". A "Save Protocol" button is located at the bottom right. A red box highlights the "Update primary design" checkbox, which is checked.

**Step 5:** After the update a pop-up window will appear asking to restart the switch, select **NO** and go to the next step.

**Step 6:** Now repeat Steps 2 through 5 again with the file name “**DSGNIO8V2.ffw**”, same method with doing the dual-update.

**Step 7:** Perform a cold start of the matrix to activate the updated FPGA designs. Press the button **YES** on the appearing pop-up window to shut down the matrix.



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**Step 8:** Start your Matrixes. If you have a matrix grid, start your intended grid master first.

**Step 9:** Finally, update your system using the provided “[Step2\\_20180301\\_V0305.tfw](#)”