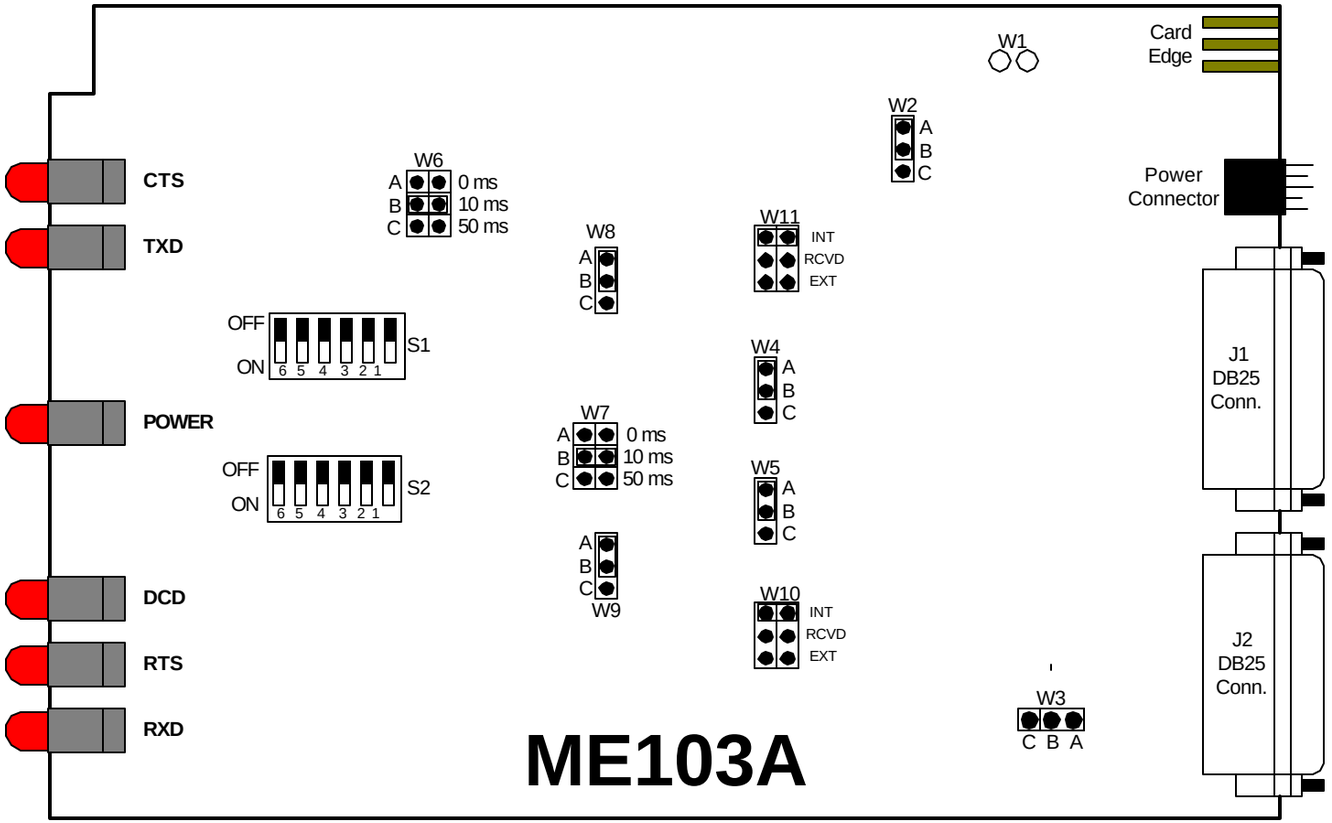

Data Rate	Switch S1 Position						Switch S2 Position					
	1	2	3	4	5	6	1	2	3	4	5	6
1200	Off	On	Off	Off	Off	On	On	On	On	On	On	On
2400	ON	Off	Off	Off	On	Off	On	On	On	On	On	Off
4800	Off	Off	Off	On	Off	Off	On	On	On	On	Off	On
9600	Off	Off	On	Off	Off	Off	On	On	On	Off	On	Off
19200	Off	On	Off	Off	Off	Off	On	On	Off	On	Off	Off
38400	On	Off	Off	Off	Off	Off	On	Off	On	Off	Off	Off
48000	Off	Off	Off	Off	Off	Off	On	On	On	Off	On	On
56000	Off	Off	Off	Off	Off	Off	On	On	On	On	Off	On
64000	Off	Off	Off	Off	Off	Off	On	Off	Off	On	Off	On
76800	Off	Off	Off	Off	Off	Off	Off	On	Off	Off	Off	On
84000	Off	Off	Off	Off	Off	Off	On	On	On	On	On	Off
96000	Off	Off	Off	Off	Off	Off	On	On	Off	On	On	Off
112000	Off	Off	Off	Off	Off	Off	On	On	On	Off	On	Off
128000	Off	Off	Off	Off	Off	Off	Off	Off	On	Off	On	Off
192000	Off	Off	Off	Off	Off	Off	On	Off	On	On	Off	Off
224000	Off	Off	Off	Off	Off	Off	On	On	Off	On	Off	Off
336000	Off	Off	Off	Off	Off	Off	On	On	On	Off	Off	Off
384000	Off	Off	Off	Off	Off	Off	Off	On	On	Off	Off	Off
448000	Off	Off	Off	Off	Off	Off	On	Off	On	Off	Off	Off
896000	Off	Off	Off	Off	Off	Off	Off	On	Off	Off	Off	Off
1344000	Off	Off	Off	Off	Off	Off	On	Off	Off	Off	Off	Off

Specifications:

Interface: RS-530
Clock: Internal or External from either attached device. (user-selectable)

Data Rate: Any speed up to 1.344 Mbps that evenly divides 2.688 Mbps. (user-selectable)

Maximum Distance: Up to 2000 ft. on each side, depending on cable quality and data rate.

Connectors: SME-530: (2) DB25 Female.
Power: 120VAC Standalone Models. Power Supply part # is PS146. Output = 17 VAC CT 700 ma. 230VAC = PS146E. 230VAC 50 HZ @ 50 ma; output = 17 VAC CT @ 700 ma.

Indicators: (6) LED's: TXD, RXD RTS, CTS, DCD, and Power

Strap Settings:

W1: This strap is used to tie the chassis ground (Pin 1) and signal ground (Pin 19) together. The unit comes from the factory without the grounds tied common.
W2: Test Mode (TM) on J1 (pin 25) is either connected to -5V (A-B) or +5V (B-C)
W3: Test Mode (TM) on J2 (pin 25) is either connected to -5V (A-B) or +5V (B-C)
W4: In the A-B position, DCE, Ready on J1 (pins 6 and 22) is "OFF" and connected to DTE Ready (pins 20 and 23). In the B-C position, DCE Ready is "ON".
W5: In the A-B position, DCE Ready on J1 (pins 6 and 22) is "OFF" and connected to DTE Ready (pins 20 and 23). In the B-C position, DCE Ready is "ON"
W6 and W7: Sets the Clear to Send delay on J1and J2: Position A = 0 ms(no delay); Position B = 10 ms; Position C = 50 ms
W8: In the A-B position, Received Line Signal Detector (RLSD) on J2 (pins 8 and 10) is derived from Request to Send on J1 (pins 4 and 19). In the B-C position, RLSD on J2 is constantly held "ON".
W9: In the A-B position, Received Line Signal Detector (RLSD) on J1 (pins 8 and 10) is derived from Request to Send on J2 (pins 4 and 19). In the B-C position, RLSD on J1 is constantly held "ON".
W10: Determines the clock source for J1 (pins 12 and 15-Transmitter Signal Element Timing DCE-on J1, Pins 9 and 17-Receiver Signal Element Timing DCE-on J2). In the A position, timing is internal (from the SME's internal clock). In the B position, timing is recovered (from J2's pins 11 and 24, Transmitter Signal Element Timing DTE). In the C position, timing is external (from J1's pins 11 and 24, Transmitter Signal Element Timing DTE).
W11: Determines the clock source for J2 (pins 12 and 15-Transmitter Signal Element Timing DCE-on J2, pins 9 and 17-Receiver Signal Element Timing DCE-on J1). In the A position, timing is internal (from the SME's internal clock). In the B position, timing is recovered (from J1's pins 11 and 24, Transmitter Signal Element Timing DTE). In the C position, timing is external (from J2's pins 11 and 24, Transmitter Signal Element Timing DTE).