





## SPECIFICATIONS:

**ME620A** 

**<u>Transmission</u>**: Asynchronous and Synchronous.

**<u>Clocking</u>**: Internal, External or derived from receive signal.

Distance: Up to 3 miles (4.8 km).

- Data Rates: Asynchronous: 2.4 to 38.4 Kbps; Synchronous: 2.4, 9.6, 14.4, 28.8, 38.4, 56, 64, 72, 128, 144, 192, 256 Kbps.
- Handshaking: Software (X-ON/X-OFF) or hardware (RTS/CTS). Both modes available at all times.

Connectors: (1) Fiber ST

- Link Budget: 12 dB (62.5 micron), 8 dB (50 micron)
- Diagnostics: V.54 Compliant; Local Analog Loopback and Remote Digital Loopback bit-error-rate pattern
- Fiber Modes: Single 50- or 62.5 micron core, multi-mode fiberoptic cable.

LED Indicators: TD, RD, RTS, DTR, ER, NS and TM.

Interface Modules: EIA RS-232/CCITT V.24, RS-232/530, CCITT V.35, X.21, Ethernet Bridge and G.703.

Power: 85 to 256 VAC universal-input power supply, 50/60 Hz.

## **CONFIGURATION OF S1:**

S1-1 through S1-4 set two parameters: synchronous or asynchronous data rate and the maximum transmission distance between two Model ME620A's.

| <u>S1-1</u> | <u>S1-2</u> | <u>S1-3</u> | <u>S1-4</u> | <u>Data Rate</u><br>(Kbps) | <u>Max Distance</u><br>in mile (km): |              |
|-------------|-------------|-------------|-------------|----------------------------|--------------------------------------|--------------|
| ON          | OFF         | OFF         | ON          | 0-19.2                     | 3.0 (4.8)                            | ASYNC        |
| ON          | OFF         | OFF         | OFF         | 0-38.4                     | 1.5 (2.4)                            | SETTINGS     |
| ON          | ON          | ON          | OFF         | 2.4                        | 1.5 (2.4)                            |              |
| OFF         | ON          | ON          | ON          | 9.6                        | 3.0 (4.8)                            |              |
| ON          | OFF         | ON          | OFF         | 14.4                       | 1.5 (2.4)                            |              |
| ON          | OFF         | ON          | ON          | 19.2                       | 3.0 (4.8)                            | <b>`</b>     |
| OFF         | OFF         | ON          | OFF         | 28.8                       | 1.5 (2.4)                            | $\backslash$ |
| OFF         | OFF         | ON          | ON          | 38.4                       | 3.0 (4.8)                            | $\mathbf{i}$ |
| ON          | ON          | OFF         | ON          | 48.0                       | 3.0 (4.8)                            | SYNC         |
| OFF         | ON          | OFF         | OFF         | 56.0                       | 1.5 (2.4)                            | SETTINGS     |
| OFF         | OFF         | OFF         | ON          | 64.0                       | 3.0 (4.8)                            |              |
| OFF         | ON          | ON          | OFF         | 72.0                       | 1.5 (2.4)                            |              |
| OFF         | OFF         | OFF         | OFF         | 128.0                      | 1.5 (4.8)                            | /            |
| ON          | ON          | OFF         | OFF         | 144.0                      | 1.5 (2.4)                            |              |
| OFF         | ON          | OFF         | ON          | 192.0                      | 1.5 (2.4)                            |              |
| ON          | ON          | ON          | ON          | 256.0                      | 1.5 (2.4)                            |              |

**S1-5 and S1-6:** Clock Source are set in combination to determine the transmit clock source.

| <u>S1-5</u> | <u>S1-6</u> | SETTING:         |
|-------------|-------------|------------------|
| ON          | OFF         | Internal         |
| ON          | ON          | Internal         |
| OFF         | ON          | External         |
| OFF         | OFF         | Received Recover |
|             |             |                  |

DESCRIPTION: Transmit clock generated internally Transmit clock generated internally Transmit clock derived from terminal interface Transmit clock derived from the received line signal Switch S1-7: DTE Control of DL determines whether DTE control of remote digital loopback test is enabled or disabled. If DTE control is disabled, the DL test can only be initiated by the front-panel switch. If DTE control is not available, set switch S1-7 to the OFF position.

| <u>S1-7</u> | SETTING: | DESCRIPTION:                    |
|-------------|----------|---------------------------------|
| ON          | Enabled  | Respond to DL requests from DTE |
| OFF         | Disabled | Ignore DL requests from the DTE |

Switch S1-8: DTE Control of LAL determines whether DTE control of local analog loopback test is enabled or disabled. If DTE control is disabled, the LAL test can only be initiated by the front-panel switch. If DTE control is not available, set switch S1-8 to the OFF position.

| <u>S1-8</u> | SETTING: |
|-------------|----------|
| ON          | Enabled  |

OFF

## DESCRIPTION:

Enabled Respond to LAL requests from DTE Disabled Ignore LAL requests from the DTE