

ME621A

SPECIFICATIONS:

Transmission: Asynchronous and Synchronous.

Clocking: Internal, External or derived from receive signal.

Distance: Up to 3 miles (4.8 km).

<u>Data Rates:</u> Asynchronous: 0 to 19.2 Kbps; Synchronous: 4.8, 9.6, 14.4, 19.2, 28.8, 32, 56, 64, 128, Kbps

Handshaking: Software (X-ON/X-OFF) or hardware (RTS/CTS). Both modes available

at all times.

Connectors: (1) Fiber ST

Link Budget: 12 dB (62.5 micron), 8 dB (50 micron)

<u>Diagnostics:</u> V.54 Compliant; Local Analog Loopback and Remote Digital Loopback

bit-error-rate pattern

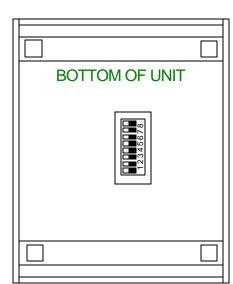
Fiber Modes: Single 50- or 62.5 micron core, multi-mode fiberoptic cable.

LED Indicators: TD, RD, RTS, DTR, ER, NS and TM.

Interface Modules: EIA RS-232/CCITT V.24, RS-232/530, CCITT V.35, X.21,

Ethernet Bridge and G.703.

Power: 85 to 256 VAC universal-input power supply, 50/60 Hz.





CONFIGURATION OF S1:

S1-1 through S1-4 set two parameters: synchronous or asynchronous data rate and the maximum transmission distance between two Model ME620A's.

| <u>S1-1</u> | <u>S1-2</u> | <u>S1-3</u> | <u>S1-4</u> | <u>Data Rate</u> (Kbps) | Max Distance in mile (km): |
|-------------|-------------|-------------|-------------|----------------------------|-------------------------------|
| ON | OFF | OFF | ON | 0-19.2 | 3.0 (4.8) ——— ASYNC SETTINGS |
| | | | | | 3E11114G3 |
| OFF | ON | ON | OFF | 4.8 | 3.0 (4.8 _ |
| OFF | ON | ON | ON | 9.6 | 3.0 (4.8) |
| ON | OFF | ON | OFF | 14.4 | 1.5 (2.4) |
| ON | OFF | ON | ON | 19.2 | 3.0 (4.8) SYNC |
| OFF | OFF | ON | OFF | 28.8 | 1.5 (2.4) SETTINGS |
| OFF | OFF | OFF | OFF | 32.0 | 3.0 (4.8) |
| OFF | ON | OFF | OFF | 56.0 | 1.5 (2.4) |
| OFF | OFF | OFF | ON | 64.0 | 3.0 (4.8) |
| OFF | OFF | OFF | OFF | 128.0 | 3.0 (4.8) |

S1-5 and S1-6: Clock Source are set in combination to determine the transmit clock source.

| <u>S1-5</u> | <u>S1-6</u> | <u>SETTING:</u> | DESCRIPTION: |
|-------------|-------------|------------------|--|
| ON | OFF | Internal | Transmit clock generated internally |
| ON | ON | Internal | Transmit clock generated internally |
| OFF | ON | External | Transmit clock derived from terminal interface |
| OFF | OFF | Received Recover | Transmit clock derived from the received line signal |
| | | | |

Switch S1-7: DTE Control of DL determines whether DTE control of remote digital loopback test is enabled or disabled. If DTE control is disabled, the DL test can only be initiated by the front-panel switch. If DTE control is not available, set switch S1-7 to the OFF position.

<u>**S1-7**</u> ON **SETTING:** Enabled

DESCRIPTION: Respond to DL requests from DTE Disabled Ignore DL requests from the DTE

Switch S1-8: DTE Control of LAL determines whether DTE control of local analog loopback test is enabled or disabled. If DTE control is disabled, the LAL test can only be initiated by the front-panel switch. If DTE control is not available, set switch S1-8 to the OFF position.

<u>**S1-8**</u> ON **DESCRIPTION:** SETTING:

OFF

OFF

Respond to LAL requests from DTE Enabled Ignore LAL requests from the DTE Disabled